

**CHAUDHARY DEVI LAL UNIVERSITY, SIRSA**  
(Established by the State Legislature Act 9 of 2003)



**NOTIFICATION**

(Through email)

The Vice-Chancellor has pleased to constitute the following Adhoc Board (common for UG/PG) for the Electrical Engineering:

**Electrical Engineering:**

Sr. no.	Proposed	Designation
(i)	Dean Faculty of Engineering & Technology, CDLU, Sirsa.	Chairperson/ Member
(ii)	<b>Two teachers of Subject from the affiliated Colleges:</b> 1. Sh. Puneet Chawla, Assistant Professor, CDL State Institute of Eng. & Tech. Panniwala Mota, Sirsa. (Term for two years w.e.f. date of notification).	Member
(iii)	<b>One Outside Subject Expert:</b> Dr. Priti Prabhakar, Associate Professor, Department of Electrical Engineering, GJUS&T, Hisar, Mob: 989951170. Email: <a href="mailto:priti.prabhakar@gmail.com">priti.prabhakar@gmail.com</a> (Term for two years w.e.f. date of notification).	Member

  
03/02/2023  
Deputy Registrar (Acad.)  
For Registrar

dated: 03/2/23

Endst. No./Acad./AC-II/70/23/7223-7230

Copy of the above is forwarded to the following for information and further necessary action:-

1. All the members of the above committee.
2. The Dean, Faculty of Engg. & Technology, Chaudhary Devi Lal University, Sirsa.
3. The Chairperson, Department of CSE, Chaudhary Devi Lal University, Sirsa.
4. The Principal, CDL State Institute of Eng. & Tech. Panniwala Mota, Sirsa.
5. The Director, UITDC, CDLU, Sirsa with the request to upload the above notification on the University Website.
6. P.S. to Vice-Chancellor (for kind information of the Vice-Chancellor), Chaudhary Devi Lal University, Sirsa.
7. P.A. to Registrar (for kind information of the Registrar), Chaudhary Devi Lal University, Sirsa.

  
03/02/23  
Superintendent(Acad.)